



101 647 664

UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee : Tony Mai

Patent No.: US 7,477,716 B2 Issued: January 13, 2009

Title: START UP CIRCUIT FOR DELAY LOCKED LOOP

CERTIFICATE OF MAILING OR TRANSMISSION	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, or is being facsimile transmitted to the United States Patent and Trademark Office on:	
FEB. 11 2009 Date	<i>Christine A. Budd</i> Signature
Christine A. Budd	
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REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
FEB 19 2009
of Correction

Sir:

Pursuant to 35 U.S.C. § 254, we hereby request a Certificate of Correction be issued for the above-referenced U.S. Letters Patent to correct errors as shown below:

Column 10, Claim 4, lines 45-46, "to enable change in the delay in die opposite direction, the input of die second latch coupled to the output of the" should read --to enable change in the delay in the opposite direction, the input of the second latch coupled to the output of the--;

Column 11, Claim 10, line 20, "back clock, a delay, circuit providing the delay being" should read – back clock, a delay circuit providing the delay being--;


Column 12, Claim 15, line 10, "after reset of the delay locked loop means for assuring than" should read -- after reset of the delay locked loop means for assuring that--.

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Since the errors were made by the U.S. Patent and Trademark Office and not by Patentee or Patentee's Attorney/Agent, it is understood that there are no additional fees for the requested Certificate of Correction. A Certificate of Correction is enclosed herewith.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By 
Benjamin J. Sparrow
Registration No.: 62,259
Telephone: (978) 341-0036
Facsimile: (978) 341-0136

Concord, MA 01742-9133

Dated: 2/11/09

FEB 19 2009

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : **US 7,477,716 B2**
DATED : **January 13, 2009**
INVENTOR(S) : **Tony Mai**

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, Claim 4, lines 45-46, delete "to enable change in the delay in die opposite direction, the input of die second latch coupled to the output of the" and insert --to enable change in the delay in the opposite direction, the input of the second latch coupled to the output of the--;

Column 11, Claim 10, line 20, delete "back clock, a delay, circuit providing the delay being" and insert --back clock, a delay circuit providing the delay being--;

Column 12, Claim 15, line 10, delete "after reset of the delay locked loop means for assuring than" and insert -- after reset of the delay locked loop means for assuring that--.

MAILING ADDRESS OF SENDER:

021005
Hamilton, Brook, Smith & Reynolds, P.C.
530 Virginia Road, P.O. Box 9133
Concord, Massachusetts 01742-9133

PATENT NO. US 7,477,716 B2

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